

REMARKS

Claims 1, 3, and 5-18 remain in the application with claim 1 having been amended hereby.

Reconsideration is respectfully requested of the objection to the drawings.

Submitted herewith is a Replacement Sheet for FIG. 2 bearing the label "PRIOR ART."

Reconsideration is respectfully requested of the rejection of the claims under 35 U.S.C. 112, second paragraph, as being indefinite.

It is respectfully submitted that the language employed in the claims follows clearly the exemplary embodiment shown in FIG. 3, as explained in pages 5 and 6 of the present Specification.

It will be noted that the present invention relates to a system for merging data so that only a single test pin is required. Thus, as shown in FIG. 3, for example, various input signals result in only a single output signal, representing the merged output data bit that is ultimately connected to the single test pin.

Attached hereto as "Exhibit A" is a listing of the claims as previously presented, showing in handwritten notations the exact element or terminal where each of the circuit elements and signals is found in FIG. 3. In fact, the claims are also annotated in regard to FIGS. 4 and 5 in this Exhibit A.

Because the object of the invention is to provide a merged data signal, the first path circuit 310 will receive a second single data rate signal and the second path circuit 330 will receive a first single data rate signal. Thus, this leads to the merging of the data into a single output data bit.

Accordingly, upon a reading of the Specification at pages 5 and 6 and a review of FIG. 3 using Exhibit A, it is respectfully submitted that the claims are clear and definite in their recitation of the present invention and meet all requirements of 35 U.S.C. 112.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,



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